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Windows 8: The Missing Manual David Pogue,2013-02-27 With Windows 8, Microsoft completely reimagined the graphical user interface for its operating system, and designed it to run on tablets as well as PCs. It's a big change that calls for a trustworthy guide—Windows 8: The Missing Manual. New York Times columnist David Pogue provides technical insight, lots of wit, and hardnosed objectivity to help you hit the ground running with Microsoft's new OS. This jargon-free book explains Windows 8 features so clearly—revealing which work well and which don't—that it should have been in the box in the first place.

Conducting Astronomy Education Research Janelle M. Bailey,Timothy F. Slater,Stephanie J. Slater,2010-09 This book is written for astronomers who want to learn more about how science education research is done and how to begin studying the teaching and learning of astronomy. The book provides fruitful research designs and effective data collection and analysis strategies, and points readers to avenues for publishing scholarly work in astronomy education research--Back cover.

Digital Signal Processing Laboratory Nasser Kehtarnavaz,Sidharth Mahotra,2010 Field Programmable Gate Arrays (FPGAs) are increasingly becoming the platform of choice to implement DSP algorithms. This book is designed to allow DSP students or DSP engineers to achieve FPGA implementation of DSP algorithms in a one-semester DSP laboratory course or in a short design cycle time based on the LabVIEW FPGA Module. Features: - The first DSP laboratory book that uses the FPGA platform instead of the DSP platform for implementation of DSP algorithms - Incorporating introductions to LabVIEW and VHDL - Lab experiments covering FPGA implementation of basic DSP topics including convolution, digital filtering, fixed-point data representation, adaptive filtering, frequency domain processing - Hardware FPGA implementation applications including wavelet transform, software-defined radio, and MP3 player - Website providing downloadable LabVIEW FPGA codes

IBM Parallel Environment (PE) Developer Edition Dino Quintero,Akmal Chaudhri,Feng Dong,Jose Higino,Peter Mayes,Kleber Sacilotto de Souza,Wainer dos Santos Moschetta,Xiao Tian Xu,IBM Redbooks,2013-02-12 This publication helps strengthen the position of IBM® software solutions and enables for High Performance Computing (hardware, software, and tools) with a well-defined and documented deployment model within an IBM environment. As a result, customers receive a planned foundation for dynamic infrastructure for parallel High Performance Computing (HPC) applications. This IBM

Redbooks® publication addresses topics to take advantage of the strengths of IBM PE Developers Edition for HPC applications. The objective is to solve customer's challenges and maximize systems' throughput, performance, and management. This publication examines the tools, utilities, documentation, and other resources available to help the IBM technical teams provide solutions and support for IBM HPC solutions in an IBM hardware environment. This IBM Redbooks is targeted toward technical professionals (consultants, technical support staff, IT Architects, and IT Specialists) responsible for providing HPC solutions and support.

Automating with SIMATIC Hans Berger,2012-10-10 Totally Integrated Automation is the concept by means of which SIMATIC controls machines, manufacturing systems and technical processes. Taking the example of the SIMATIC S7 programmable controller, this book provides a comprehensive introduction to the architecture and operation of a state-of-the-art automation system. It also gives an insight into configuration and parameter setting for the controller and the distributed I/O. Communication via network connections is explained, along with a description of the available scope for operator control and monitoring of a plant. The new engineering framework TIA Portal combines all the automation software tools in a single development environment. Inside the TIA Portal, SIMATIC STEP 7 Professional V11 is the comprehensive engineering package for SIMATIC controllers. As the central engineering tool, STEP 7 manages all the necessary tasks, supports programming in the IEC languages LAD, FBD, STL, S7-SCL and S7-GRAPH, and also contains S7-PLCSIM for offline tests. As well as updating the previously-depicted components, this edition also presents new SIMATIC S7-1200 hardware components for PROFIBUS and PROFINET. In addition to the STEP 7 V5.5 engineering software, now STEP 7 Professional V11 is also described, complete with its applications inside TIA Portal. The book is ideally suited to all those, who, despite little previous knowledge, wish to familiarize themselves with the topic of programmable logic controllers and the architecture and operation of automation systems.

Rapid Prototyping of Digital Systems James O. Hamblen,Tyson S. Hall,Michael D. Furman,2007-10-31 Here is a laboratory workbook filled with interesting and challenging projects for digital logic design and embedded systems classes. The workbook introduces you to fully integrated modern CAD tools, logic simulation, logic synthesis using hardware description languages, design hierarchy, current generation field programmable gate array technology, and SoPC design. Projects cover such areas as serial communications, state machines with video output, video games and graphics, robotics, pipelined RISC processor cores, and designing computer systems using a commercial processor core.

DS8000 I/O Priority Manager Gero Schmidt,Bertrand Dufrasne,Jana Jamsek,Peter Kimmel,Hiroaki Matsuno,Flavio Morais,Lindsay Oxenham,Antonio Rainero,Denis Senin,IBM Redbooks,2012-03-21 This IBM® RedpaperTM publication describes the concepts and functions of the IBM System Storage® DS8000® I/O Priority Manager. The DS8000 I/O Priority Manager enables more effective storage consolidation and performance management combined with the ability to align

quality of service (QoS) levels to separate workloads in the system. With DS8000 I/O Priority Manager, the system can prioritize access to system resources to achieve the volume's desired QoS based on defined performance goals (high, medium, or low) of any volume. I/O Priority Manager constantly monitors and balances system resources to help applications meet their performance targets automatically, without operator intervention. Starting with DS8000 Licensed Machine Code (LMC) level R6.2, the DS8000 I/O Priority Manager feature supports open systems and IBM System z®. DS8000 I/O Priority Manager, together with IBM z/OS® Workload Manager (WLM), provides more effective storage consolidation and performance management for System z systems. Now tightly integrated with Workload Manager for z/OS, DS8000 I/O Priority Manager improves disk I/O performance for important workloads. It also drives I/O prioritization to the disk system by allowing WLM to give priority to the system's resources automatically when higher priority workloads are not meeting their performance goals. Integration with zWLM is exclusive to DS8000 and System z systems. The paper is aimed at those who want to get an understanding of the DS8000 I/O Priority Manager concept and its underlying design. It provides guidance and practical illustrations for users who want to exploit the capabilities of the DS8000 I/O Priority Manager.

Implementing the IBM Storwize V7000 with IBM Spectrum Virtualize V8.2.1 Jon Tate,Jack Armstrong,Tiago Bastos,Pawel Brodacki,Frank Enders,Sergey Kubin,Hartmut Lonzer,Danilo Miyasiro,Rodrigo Suzuki,IBM Redbooks,2019-11-07 Continuing its commitment to developing and delivering industry-leading storage technologies, IBM® introduces the IBM Storwize® V7000 solution powered by IBM SpectrumTM Virtualize. This innovative storage offering delivers essential storage efficiency technologies and exceptional ease of use and performance, all integrated into a compact, modular design that is offered at a competitive, midrange price. The IBM Storwize V7000 solution incorporates some of the top IBM technologies that are typically found only in enterprise-class storage systems, which raises the standard for storage efficiency in midrange disk systems. This cutting-edge storage system extends the comprehensive storage portfolio from IBM and can help change the way organizations address the ongoing information explosion. This IBM Redbooks® publication introduces the features and functions of the IBM Storwize V7000 and IBM Spectrum VirtualizeTM V8.2.1 system through several examples. This book is aimed at pre-sales and post-sales technical support and marketing and storage administrators. It helps you understand the architecture of the Storwize V7000, how to implement it, and how to take advantage of its industry-leading functions and features.

Lisp Lore: A Guide to Programming the Lisp Machine H. Bromley,2013-03-14 This book had its genesis in the following piece of computer mail: From allegra!joan-b Tue Dec 18 09:15:54 1984 To: sola!hjb Subject: lispm Hank, I've been talking with Mark Plotnik and Bill Gale about asking you to conduct a basic course on using the lisp machine. Mark, for instance, would really like to cover basics like the flavor system, etc., so he could start doing his own programming without a lot of trial and error, and Bill and I would be interested in this, too. I'm quite sure that Mark Jones, Bruce, Eric and Van

would also be really interested. Would you like to do it? Bill has let me know that if you'd care to set something up, he's free to meet with us anytime this week or next (although I'll only be here on Wed. next week) so we can come up with a plan. What do you think? Joan.

Data Acquisition Using LabVIEW Behzad Ehsani,2016-12-14 Transform physical phenomena into computer-acceptable data using a truly object-oriented language About This Book Create your own data acquisition system independently using LabVIEW and build interactive dashboards Collect data using National Instrument's and third-party, open source, affordable hardware Step-by-step real-world examples using various tools that illustrate the fundamentals of data acquisition Who This Book Is For If you are an engineer, scientist, experienced hobbyist, or student, you will highly benefit from the content and examples illustrated in this book. A working knowledge of precision testing, measurement instruments, and electronics, as well as a background in computer fundamentals and programming is expected. What You Will Learn Create a virtual instrument which highlights common functionality of LabVIEW Get familiarized with common buses such as Serial, GPIB, and SCPI commands Staircase signal acquisition using NI-DAQmx Discover how to measure light intensity and distance Master LabVIEW debugging techniques Build a data acquisition application complete with an installer and required drivers Utilize open source microcontroller Arduino and a 32-bit Arduino compatible Uno32 using LabVIEW programming environment In Detail NI LabVIEW's intuitive graphical interface eliminates the steep learning curve associated with text-based languages such as C or C++. LabVIEW is a proven and powerful integrated development environment to interact with measurement and control hardware, analyze data, publish results, and distribute systems. This hands-on tutorial guide helps you harness the power of LabVIEW for data acquisition. This book begins with a quick introduction to LabVIEW, running through the fundamentals of communication and data collection. Then get to grips with the auto-code generation feature of LabVIEW using its GUI interface. You will learn how to use NI-DAQmax Data acquisition VIs, showing how LabVIEW can be used to appropriate a true physical phenomenon (such as temperature, light, and so on) and convert it to an appropriate data type that can be manipulated and analyzed with a computer. You will also learn how to create Distribution Kit for LabVIEW, acquainting yourself with various debugging techniques offered by LabVIEW to help you in situations where bugs are not letting you run your programs as intended. By the end of the book, you will have a clear idea how to build your own data acquisition system independently and much more. Style and approach A hands-on practical guide that starts by laying down the software and hardware foundations necessary for subsequent data acquisition-intensive chapters. The book is packed full of specific examples with software screenshots and schematic diagrams to guide you through the creation of each virtual instrument.

Simple Computer Tune-up CA,2007-04-23 Forgoing the techie-talk, CA explains how PC Pitstop Optimize performs four main functions: it removes junk files to free up disk space, adjusts Internet settings for better performance, removes invalid

registry keys, and adjusts several registry settings that can improve overall performance Also shows how PC Pitstop Optimize can revitalize system performance, regain hard drive space, rev up an Internet connection, and fix registry errors CA will include its complete PC Pitstop Optimize, valued at $30.00, free with this book (no renewal needed) Note: CD-ROM/DVD and other supplementary materials are not included as part of eBook file.

PC Mag ,1996-10-22 PCMag.com is a leading authority on technology, delivering Labs-based, independent reviews of the latest products and services. Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology.

The Definitive Guide to How Computers Do Math Clive Maxfield,Alvin Brown,2005-09-27 The Basics of Computer Arithmetic Made Enjoyable and Accessible-with a Special Program Included for Hands-on LearningPacked with nuggets of information and tidbits of trivia, How Computers Do Math provides an incredibly fun and interesting introduction to the way in which computers perform their magic in general and math in particular.

Microsoft Windows 95 Neil J. Salkind,1996 Part of the Illustrated Series, this text offers a quick, visual way to build Microsoft Windows 95 skills. Covers beginning through intermediate skills.

Designer's Guide to the Cypress PSoC Robert Ashby,2005-07-28 Systems-on-chip (SoCs) are one of the hottest areas in the semiconductor industry today, replacing the ubiquitous microcontroller in many embedded applications. These powerful components combine the microprocessor, memories, and peripherals in short, the whole system on a single piece of silicon. Due to its low price and extreme flexibility, one of the most popular and innovative SoCs available is Cypress's software-reconfigurable Programmable SoC(PSoC). The PSoC has blocks of digital and analog logic that can be reconfigured to perform multiple functions, which makes it useful in a broad range of embedded system applications. This book is about designing, programming, and developing with the PSoC. As with other microcontroller titles in our Embedded Technology series, this book offers a more complete combination of technical data, example code, and descriptive prose than is available from manufacturer reference information, and is useful to both professionals and hobbyists.Intended for embedded engineers who are new to the embedded field, or for the thousands of engineers who have experience with other microcontrollers but are new to programmable SoCs, the book offers a thorough and practical description of the device features, gives development guidelines, and provides design examples. Code examples are used in virtually every chapter, and are included with the book on the companion CD-ROM. *The first independent technical reference available on the pSoC, a product line experiencing explosive growth in the embedded design world *Application examples, sample code, and design tips and techniques will get readers get up-to-speed quickly *Companion cd-rom includes all example code from book, so that engineers can easily adapt it to their own designs

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IBM FlashSystem 9100 Architecture, Performance, and Implementation Jon Tate,Andrew Greenfield,Jon Herd,Corne Lottering,Tony Pacheco,Jagadeesh Papaiah,Thomas Ploski,Stephen Solewin,Leandro Torolho,Alexander Watson,IBM Redbooks,2020-12-02 IBM® FlashSystem 9100 combines the performance of flash and Non-Volatile Memory Express (NVMe) with the reliability and innovation of IBM FlashCore® technology and the rich features of IBM SpectrumTM Virtualize — all in a powerful 2U storage system. Providing intensive data driven multi-cloud storage capacity, FlashSystem 9100 is deeply integrated with the software-defined capabilities of IBM Spectrum StorageTM, which allows you to easily add the multi-cloud solutions that best support your business. In this IBM Redbooks® publication, we discuss the product's features and planning steps, architecture, installation, configuration, and hints and tips.

Embedded Microprocessor System Design using FPGAs Uwe Meyer-Baese,2021-04-16 This textbook for courses in Embedded Systems introduces students to necessary concepts, through a hands-on approach. It gives a great introduction to FPGA-based microprocessor system design using state-of-the-art boards, tools, and microprocessors from Altera/Intel® and Xilinx®. HDL-based designs (soft-core), parameterized cores (Nios II and MicroBlaze), and ARM Cortex-A9 design are discussed, compared and explored using many hand-on designs projects. Custom IP for HDMI coder, Floating-point operations, and FFT bit-swap are developed, implemented, tested and speed-up is measured. Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze, MicroBlaze, Nios II and ARMv7 architectures in VHDL and Verilog code, as well as the custom IP projects. Each Chapter has a substantial number of short quiz questions, exercises, and challenging projects. Explains soft, parameterized, and hard core systems design tradeoffs; Demonstrates design of popular KCPSM6 8 Bit microprocessor step-by-step; Discusses the 32 Bit ARM Cortex-A9 and a basic processor is synthesized; Covers design flows for both FPGA Market leaders Nios II Altera/Intel and MicroBlaze Xilinx system; Describes Compiler-Compiler Tool development; Includes a substantial number of Homework's and FPGA exercises and design projects in each chapter.

FPGAs 101 Gina Smith,2010-01-16 FPGAs (Field-Programmable Gate Arrays) can be found in applications such as smart phones, mp3 players, medical imaging devices, and for aerospace and defense technology. FPGAs consist of logic blocks and

programmable interconnects. This allows an engineer to start with a blank slate and program the FPGA for a specific task, for instance, digital signal processing, or a specific device, for example, a software-defined radio. Due to the short time to market and ability to reprogram to fix bugs without having to respin FPGAs are in increasingly high demand. This book is for the engineer that has not yet had any experience with this electrifying and growing field. The complex issue of FPGA design is broken down into four distinct phases - Design / Synthesis / Simulation / Place & Route. Numerous step-by-step examples along with source code accompany the discussion. A brief primer of one of the popular FPGA and hardware languages, VHDL, is incorporated for a simple yet comprehensive learning tool. While a general technology background is assumed, no direct hardware development understanding is needed. Also, included are details on tool-set up, verifaction techniques, and test benches. Reference material consists of a quick reference guide, reserved words, and common VHDL/FPGA terms. Learn how to design and develop FPGAs -- no prior experience necessary! Breaks down the complex design and development of FPGAs into easy-to-learn building blocks Contains examples, helpful tips, and step-by-step tutorials for synthesis, implementation, simulation, and programming phases

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