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Lightship Software

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CMOS Current Amplifiers Giuseppe Palmisano, Gaetano Palumbo, Salvatore Pennisi, 2012-12-06 CMOS Current Amplifiers presents design strategies for high performance current amplifiers based on CMOS technology. After an introduction to various architectures of operational amplifiers, the operating principles of the current amplifier are outlined. This book provides the reader with simple and compact design equations for use in a pencil and paper design and the following simulation step. Chapter 1 introduces the general aspects of current amplifiers. After a preliminary classification of operational amplifiers, ideal blocks and models are discussed for different architectures and a first high-level comparison is made between traditional amplifiers and current amplifiers. Analysis and examples of basic circuits, as well as signal processing applications involving current amplifiers, are also given. Non-idealities and second-order effects causing limitations in performance are then discussed and evaluated. Chapter 2 focuses on low-drive current amplifiers. Several design examples for current conveyors and class A current amplifiers are discussed in detail and design equations are presented for the main performance parameters, which allows a good trade-off between requirements. High-performance solutions for high bandwidth and low voltage capability are also considered, and, finally, current comparators with progressively enhanced performance are reported and analyzed critically. Chapter 3 deals with current amplifiers for off-chip loads. Several class AB current-mode output stages are discussed and design strategies which improve performance are presented. A detailed analysis of non-ideal effect is carried out with particular emphasis on linearity. Design examples are given and circuit arrangements for further developments are included. CMOS Current Amplifiers serves as an excellent reference for researchers and professionals of analog IC design, and may also be used as an advanced text on current amplifiers.

Formal Modeling: Actors; Open Systems, Biological Systems Gul Agha, Olivier Danvy, José Meseguer, 2011-10-30 This Festschrift volume, published in honor of Carolyn Talcott on the occasion of her 70th birthday, contains a collection of papers presented at a symposium held in Menlo Park, California, USA, in November 2011. Carolyn Talcott is a leading researcher and mentor of international renown among computer scientists. She has made key contributions to a number of areas of computer science including: semantics and verification of programming languages; foundations of actor-based systems;

middleware, meta-architectures, and systems; Maude and rewriting logic; and computational biology. The 21 papers presented are organized in topical sections named: Essays on Carolyn Talcott; actors and programming languages; cyberphysical systems; middleware and meta-architectures; formal methods and reasoning tools; and computational biology.

RapidIO Sam Fuller,2004-12-27 RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major reference work for the evaluation and understanding of RapidIO. Covering essential aspects of the specification, it also answers most usage questions from both hardware and software engineers. It will also serve as a companion text to the specifications when developing or working with the RapidIO interconnect technology. Including the history of RapidIO and case of studies of RapidIO deployment, this really is the definitive reference guide for this new area of technology.

Optical, Electronic Materials and Applications IV Ke Xiang Wei,Yu Hang Yang,2013-07-31 Selected, peer reviewed papers from the 4th International Conference on Optical, Electronic Materials and Applications 2013 (OEMA 2013), March 22-24, 2013, Chongqing, China

Analysis of Bipolar and CMOS Amplifiers Amir M. Sodagar,2018-10-08 The classical approach to analog circuit analysis is a daunting prospect to many students, requiring tedious enumeration of contributing factors and lengthy calculations. Most textbooks apply this cumbersome approach to small-signal amplifiers, which becomes even more difficult as the number of components increases. Analysis of Bipolar and CMOS Amplifiers offers students an alternative that enables quick and intuitive analysis and design: the analysis-by-inspection method. This practical and student-friendly text demonstrates how to achieve approximate results that fall within an acceptable range of accuracy and are based on sound scientific principles. Working from the basics of amplifiers and transistors to biasing, single- and multistage amplifiers, current sources and mirrors, and analysis at midband, low, and high frequencies, the author demonstrates the interrelationship between behavior in both the time and frequency domains and balances the discussion between bipolar and CMOS circuits. Each chapter closes with a set of simulation examples in SPICE and MATLAB® that give students hands-on experience applying the concepts and methods using industry-standard tools. Building a practical working knowledge around a solid theoretical framework, Analysis of Bipolar and CMOS Amplifiers prepares your students to meet the challenges of quick and accurate approximations and software-based analysis awaiting them in the workplace.

CMOS Analog Design Using All-Region MOSFET Modeling Márcio Cherem Schneider,Carlos Galup-Montoro,2010-01-28 Covering the essentials of analog circuit design, this book takes a unique design approach based on a MOSFET model valid for all operating regions, rather than the standard square-law model. Opening chapters focus on device modeling, integrated circuit technology, and layout, whilst later chapters go on to cover noise and mismatch, and analysis and design of the basic building blocks of analog circuits, such as current mirrors, voltage references, voltage amplifiers, and operational amplifiers.

An introduction to continuous-time filters is also provided, as are the basic principles of sampled-data circuits, especially switched-capacitor circuits. The final chapter then reviews MOSFET models and describes techniques to extract design parameters. With numerous design examples and exercises also included, this is ideal for students taking analog CMOS design courses and also for circuit designers who need to shorten the design cycle.

The Introduction to the H8 Microcontroller Yukiho Fujisawa, 2003

MacScheme Manual and Software Lightship Software, 1990 This edition of a well-respected implementation of the Scheme programming language is designed to complement textbooks that use Scheme, including Structure and Interpretation of Computer Programs. MacScheme is compatible with the most recent standards for Scheme, a language noted for its simplicity and power. The software features an integrated, easy-to-use editor, a proven byte code compiler and interpreter, debugging tools, and easy graphics. MacScheme requires an Apple Macintosh family computer with at least one megabyte of RAM.

Sound System Engineering 4e Don Davis, Eugene Patronis, Pat Brown, 2013-06-26 Long considered the only book an audio engineer needs on their shelf, Sound System Engineering provides an accurate, complete and concise tool for all those involved in sound system engineering. Fully updated on the design, implementation and testing of sound reinforcement systems this great reference is a necessary addition to any audio engineering library. Packed with revised material, numerous illustrations and useful appendices, this is a concentrated capsule of knowledge and industry standard that runs the complete range of sound system design from the simplest all-analog paging systems to the largest multipurpose digital systems.

Advances in Analog Circuits Esteban Tlelo-Cuautle, 2011-02-02 This book highlights key design issues and challenges to guarantee the development of successful applications of analog circuits. Researchers around the world share acquired experience and insights to develop advances in analog circuit design, modeling and simulation. The key contributions of the sixteen chapters focus on recent advances in analog circuits to accomplish academic or industrial target specifications.

Planar Microwave Engineering Thomas H. Lee, 2004-08-30 Sample Text

VLSI: Systems on a Chip Luis Miguel Silveira, Srinivas Devadas, Ricardo A. Reis, 2013-11-11 For over three decades now, silicon capacity has steadily been doubling every year and a half with equally staggering improvements continuously being observed in operating speeds. This increase in capacity has allowed for more complex systems to be built on a single silicon chip. Coupled with this functionality increase, speed improvements have fueled tremendous advancements in computing and have enabled new multi-media applications. Such trends, aimed at integrating higher levels of circuit functionality are tightly related to an emphasis on compactness in consumer electronic products and a widespread growth and interest in wireless communications and products. These trends are expected to persist for some time as technology and

design methodologies continue to evolve and the era of Systems on a Chip has definitely come of age. While technology improvements and spiraling silicon capacity allow designers to pack more functions onto a single piece of silicon, they also highlight a pressing challenge for system designers to keep up with such amazing complexity. To handle higher operating speeds and the constraints of portability and connectivity, new circuit techniques have appeared. Intensive research and progress in EDA tools, design methodologies and techniques is required to empower designers with the ability to make efficient use of the potential offered by this increasing silicon capacity and complexity and to enable them to design, test, verify and build such systems.

Implementations of Logic Programming Systems Evan Tick, Giancarlo Succi, 2012-12-06 This volume is a collection of research papers in the area of the implementation of logic programming systems. It will be of immediate interest to practitioners who seek an understanding of how to efficiently manage memory, generate fast code, perform sophisticated static analyses, and design high-performance runtime features. A major theme throughout the book is how to effectively leverage host implementation systems and technologies to implement target systems. The book is also beneficial for future reference because it summarizes a wealth of systems implementation experience of the researchers shaping the field over the past ten years. Another theme of the book is compilation techniques to boost performance. The field of static analysis for logic programs is a rapidly developing field that deserves a volume on its own. *Implementations of Logic Programming Systems* is an excellent reference and may be used as a text for a course on the subject.

Introduction To Modern Planar Transmission Lines Anand K. Verma, 2021-06-02 Provides a comprehensive discussion of planar transmission lines and their applications, focusing on physical understanding, analytical approach, and circuit models. Planar transmission lines form the core of the modern high-frequency communication, computer, and other related technology. This advanced text gives a complete overview of the technology and acts as a comprehensive tool for radio frequency (RF) engineers that reflects a linear discussion of the subject from fundamentals to more complex arguments. *Introduction to Modern Planar Transmission Lines: Physical, Analytical, and Circuit Models* Approach begins with a discussion of waves on transmission lines and waves in material medium, including a large number of illustrative examples from published results. After explaining the electrical properties of dielectric media, the book moves on to the details of various transmission lines including waveguide, microstrip line, co-planar waveguide, strip line, slot line, and coupled transmission lines. A number of special and advanced topics are discussed in later chapters, such as fabrication of planar transmission lines, static variational methods for planar transmission lines, multilayer planar transmission lines, spectral domain analysis, resonators, periodic lines and surfaces, and metamaterial realization and circuit models. Emphasizes modeling using physical concepts, circuit-models, closed-form expressions, and full derivation of a large number of expressions. Explains advanced mathematical treatment, such as the variation method, conformal mapping method, and SDA.

Connects each section of the text with forward and backward cross-referencing to aid in personalized self-study Introduction to Modern Planar Transmission Lines is an ideal book for senior undergraduate and graduate students of the subject. It will also appeal to new researchers with the inter-disciplinary background, as well as to engineers and professionals in industries utilizing RF/microwave technologies.

CMOS Circuits for Biological Sensing and Processing Srinjoy Mitra, David R. S. Cumming, 2017-11-18 This book provides the most comprehensive and consistent survey of the field of IC design for Biological Sensing and Processing. The authors describe a multitude of applications that require custom CMOS IC design and highlight the techniques in analog and mixed-signal circuit design that potentially can cross boundaries and benefit the very wide community of bio-medical engineers.

Netcentric System of Systems Engineering with DEVS Unified Process Saurabh Mittal, José L. Risco Martín, 2013-02-22 In areas such as military, security, aerospace, and disaster management, the need for performance optimization and interoperability among heterogeneous systems is increasingly important. Model-driven engineering, a paradigm in which the model becomes the actual software, offers a promising approach toward systems of systems (SoS) engineering. However, model-driven engineering has largely been unachieved in complex dynamical systems and netcentric SoS, partly because modeling and simulation (M&S) frameworks are stove-piped and not designed for SoS composability. Addressing this gap, Netcentric System of Systems Engineering with DEVS Unified Process presents a methodology for realizing the model-driven engineering vision and netcentric SoS using DEVS Unified Process (DUNIP). The authors draw on their experience with Discrete Event Systems Specification (DEVS) formalism, System Entity Structure (SES) theory, and applying model-driven engineering in the context of a netcentric SoS. They describe formal model-driven engineering methods for netcentric M&S using standards-based approaches to develop and test complex dynamic models with DUNIP. The book is organized into five sections: Section I introduces undergraduate students and novices to the world of DEVS. It covers systems and SoS M&S as well as DEVS formalism, software, modeling language, and DUNIP. It also assesses DUNIP with the requirements of the Department of Defense's (DoD) Open Unified Technical Framework (OpenUTF) for netcentric Test and Evaluation (T&E). Section II delves into M&S-based systems engineering for graduate students, advanced practitioners, and industry professionals. It provides methodologies to apply M&S principles to SoS design and reviews the development of executable architectures based on a framework such as the Department of Defense Architecture Framework (DoDAF). It also describes an approach for building netcentric knowledge-based contingency-driven systems. Section III guides graduate students, advanced DEVS users, and industry professionals who are interested in building DEVS virtual machines and netcentric SoS. It discusses modeling standardization, the deployment of models and simulators in a netcentric environment, event-driven architectures, and more. Section IV explores real-world case studies that realize many of the concepts defined in the previous

chapters. Section V outlines the next steps and looks at how the modeling of netcentric complex adaptive systems can be attempted using DEVS concepts. It touches on the boundaries of DEVS formalism and the future work needed to utilize advanced concepts like weak and strong emergence, self-organization, scale-free systems, run-time modularity, and event interoperability. This groundbreaking work details how DUNIP offers a well-structured, platform-independent methodology for the modeling and simulation of netcentric system of systems.

The Analysis and Design of Linear Circuits Roland E. Thomas, Albert J. Rosa, Gregory J. Toussaint, 2023-04-06 THE ANALYSIS AND DESIGN OF LINEAR CIRCUITS Textbook covering the fundamentals of circuit analysis and design, now with additional examples, exercises, and problems The Analysis and Design of Linear Circuits, 10th Edition, taps into engineering students desire to explore, create, and put their learning into practice by presenting linear circuit theory, with an emphasis on circuit analysis and how to evaluate competing designs. The text integrates active and passive linear circuits, allowing students to understand and design a wide range of circuits, solve analytical problems, and devise solutions to problems. The authors use both phasors and Laplace techniques for AC circuits, enabling better understanding of frequency response, filters, AC power, and transformers. The authors have increased the integration of MATLAB® and Multisim in the text and revised content to be up-to-date with technology when appropriate. The text uses a structured pedagogy where objectives are stated in each chapter opener and examples and exercises are developed so that the students achieve mastery of each objective. The available problems revisit each objective and a suite of problems of increasing complexity task the students to check their understanding. Topics covered in The Analysis and Design of Linear Circuits, 10th Edition, include: Basic circuit analysis, including element, connection, combined, and equivalent circuits, voltage and current division, and circuit reduction Circuit analysis techniques, including node-voltage and mesh-current analysis, linearity properties, maximum signal transfer, and interface circuit design Signal waveforms, including the step, exponential, and sinusoidal waveforms, composite waveforms, and waveform partial descriptors Laplace transforms, including signal waveforms and transforms, basic properties and pairs, and pole-zero and Bode diagrams Network functions, including network functions of one- and two-port circuits, impulse response, step response, and sinusoidal response An appendix that lists typical RLC component values and tolerances along with a number of reference tables and OP AMP building blocks that are foundational for analysis and design. With an overarching goal of instilling smart judgment surrounding design problems and innovative solutions, The Analysis and Design of Linear Circuits, 10th Edition, provides inspiration and motivation alongside an essential knowledge base. The text is designed for two semesters and is complemented with robust supplementary material to enhance various pedagogical approaches, including an Instructors Manual which features an update on how to use the book to complement the 2022-23 ABET accreditation criteria, 73 lesson outlines using the new edition, additional Instructor Problems, and a Solutions Manual. These resources can be found on the companion website:

<https://bcs.wiley.com/he-bcs/Books?action=index&bcsId=12533&itemId=1119913020>.

Optical Modulation Le Nguyen Binh, 2017-11-22 This book aims to present fundamental aspects of optical communication techniques and advanced modulation techniques and extensive applications of optical communications systems and networks employing single-mode optical fibers as the transmission system. New digital techniques such as chromatic dispersion, polarization mode dispersion, nonlinear phase distortion effects, etc. will be discussed. Practical models for practice and understanding the behavior and dynamics of the devices and systems will be included.

Prototyping Augmented Reality Tony Mullen, 2011-09-20 Learn to create augmented reality apps using Processing open-source programming language Augmented reality (AR) is used all over, and you may not even realize it. Smartphones overlay data onto live camera views to show homes for sale, restaurants, or historical sites. American football broadcasts use AR to show the invisible first-down line on the field to TV viewers. Nike and Budweiser, among others, have used AR in ads. Now, you can learn to create AR prototypes using 3D data, Processing open-source programming language, and other languages. This unique book is an easy-to-follow guide on how to do it. Guides you through the emerging technology of Augmented Reality (AR) Shows you how to use 3D data with the Processing programming environment and other languages to create AR prototypes for the web, smartphones, Macs, and PCs Helps 3D artists and designers who want to move into the AR market but don't have programming skills Covers the essentials of 3D programming, creating objects for an AR library, building and exporting 3D models, and much more Explains how to interactively link 3D to physical, virtual, and streaming environments Author Tony Mullen is both an artist and a programmer and perfectly suited to explain how to bridge these two worlds, as he so deftly does in Prototyping with Augmented Reality.

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